

U.S. Patent Application Serial No. 09/267,398

**IN THE TITLE:**

Amend the title of the invention as follows:

~~A~~ ~~Digital Camera Capable Of Image Processing Forming A Smaller Motion Image~~

Frame

**IN THE SPECIFICATION:**

Amend the specification as follows:

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**Please amend on page 13, line 22 in the specification.**

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*inv B2*  
Zoom circuit 75 is configured as shown in Fig. 8. Image data output from single Processing circuit 74 is written to a line memory 75a in response to a pixel clock and thereafter read, delayed by one line by the same pixel clock. The read image data is a grin written to another line memory 75b and read delayed by one line. Therefore, from line memories 75a and 75b, image data of continuous two lines are output simultaneously. The image data output from line memories 75a is directly imposed to a multiplexer 75g and in addition," to a multiplexer 75f delayed by one pixel by latch circuit 75c. Image data output from line memory 75b is directly and puts to multiplexer 22i and input to multiplexer 75h delayed by one pixel by latch circuit 75d.

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**Please amend on page 23, line 13 in the specification.**

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*inv B2*  
When the cursor key 86c or 86d is operated in step S41, CPU 83 reproduces another pic file from memory card 82 in step S43, and thereafter returns to step S39. In this case also, CPU 83 applies a read instruction to memory card 82 to read a desired pic file, and the compressed image stored in the pic file is decompressed by compression/decompression circuit 77. The decompressed image data are overwritten on video memory area 81a in step S38. Thus the reproduced image to be displayed on monitor 80 is updated. When the motion image reducing mode is selected, the flow proceeds to step S51, when the still image cutting mode is selected,

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A3  
and  
the flow proceeds to step S73, and when the still image paste mode is selected, the flow proceeds to step S88.

Please amend on page 24, line 32 in the specification.

A4  
A3  
and  
When the operator operates shutter button 89, CPU 83 determines that it is "YES" in step S65, and performs JPEG compression on all the image data stored in video memory area 81a in step S67. At this time, CPU 83 applies the read request and the initial frame data to memory control circuit 78, and applies the compression instruction to compression/decompression circuit 77. Consequently, compressed image data are generated. Thereafter, in step S68, instruction to form a pic file is applied, and in step S6 S69, instruction to store the compressed image data to the pic file and writing of the frame data of the monitor frame are applied. Thereafter, in step S71, the pic file in which the compressed image data are stored is stored in memory card 82, and the flow returns to step S35.

Please amend on page 24, line 20 in the specification.

A5  
and  
A3  
and  
Returning to Fig. 14, when it is determined in step S 47 that the still image cutting mode is set, CPU 83 processes the subroutine shown in Fig. 19 in step S 73 of Fig. 16, and determines the still image frame. In step S 75, still image data are read from video memory area 81a, and character data of the still image frame are read from character area 81b and output to monitor 80. Accordingly, a still image frame is synthesized and displayed on still image. When the operator operates cursor key 86a to 86d, CPU 83 determines it is "YES" in step S77, and updates the upper left coordinate data of the still image frame in step S79. More specifically, the X and Y

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AS and  
coordinates of the frame data to be applied to memory circuit 78 are updated. Thereafter, the flow returns to step S75. Accordingly, the still image frame moves on monitor 80. When the operator operates shutter button 89 in step S81, CPU 83 applies the image data in the still image frame to compression/decompression circuit 77 in step S83 and performs JPEG compression thereon. Thereafter, a syn file is formed in step S84, and the compressed image data are stored in the syn file in step S85. In step S87, the syn file storing the compressed image data is stored in memory card 82, and the flow returns to step S35.

**Please amend on page 26, line 33 in the specification.**

inv file  
In step S91, the read request and the initial frame data are applied to memory control circuit 78, and all the image data and video memory area 81a are output to monitor 80.

ALC and  
Accordingly, on the still image displayed before the setting of the still image paste mode, the still image frame is formed, and the partial still image newly reproduced from memory card 82 is synthesized within the still image frame in step S92. One operator operates cursor key 86a or 86b at this time in step S93, the partial still image is switched. CPU 83 reproduces the same pic file as the last time in step S94, and puts the initial frame data to memory control circuit 78 in step S95 and writes the reproduced image data to video memory area 81a. In steps S96 and S97, processes similar to those in steps S88 and S89 are performed, and the frame data of the still image frame are updated. Thereafter another syn file is reproduced in step S97, and the flow returns to step S91. If the partial still image to the reproduced this time is smaller than the partial still image reproduced last time, part of the still image displayed behind the partial still image

*AK*  
*cont*  
will be lost. Therefore, the same pic file is reproduced first, and thereafter another syn file is reproduced.

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**Please amend on page 27, line 25 in the specification.**

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*ind*  
*27*  
When determination key 88 is pressed in step S103 with desired two still images being displayed, CPU 80 determines that it is "YES" in step 103 and the flow proceeds to step S105.

In step S105, all image data of video memory 81a are output to monitor 80 as in step S91.

*87*  
*cont*  
Thereafter in step S106, whether any cursor key 86a to 86d is operated or not is determined and if it is a "YES", the upper left coordinates of the frame data to be applied to memory control circuit 78 are updated in step S108. Therefore, the same pic file as last time is reproduced in step S109, and the same reproduced image data are written to video memory area 81a in accordance with the initial frame data in step S110. Thereafter in step S111, the same syn as the last time is reproduced in step S111, and the reproduced image data, that is, the partial still image data are written to the still image frame which has been moved, in step S112. In step S112, the updated frame data and the write request are applied to memory control circuit 78 in the similar manner as described above, and the partial still image data are written to the form still image frame. Thereafter, the flow returns to step S105. In this manner, the displayed position of the partial still image exchange in accordance with the key operation.

**Please amend on page 28, line 10 in the specification.**

When the operator operates the shutter button 89 in step S107, CPU 83 performs compression process and on all the image data stored in the video memory area 81a in step S113,

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*AT  
once  
done* a pic file is formed in step S114, and the compressed image data are stored in pic file in step

S115. Thereafter, the pic file storing the compressed image data is stored in the memory card 82

in step S117, and the flow returns to step S35.

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